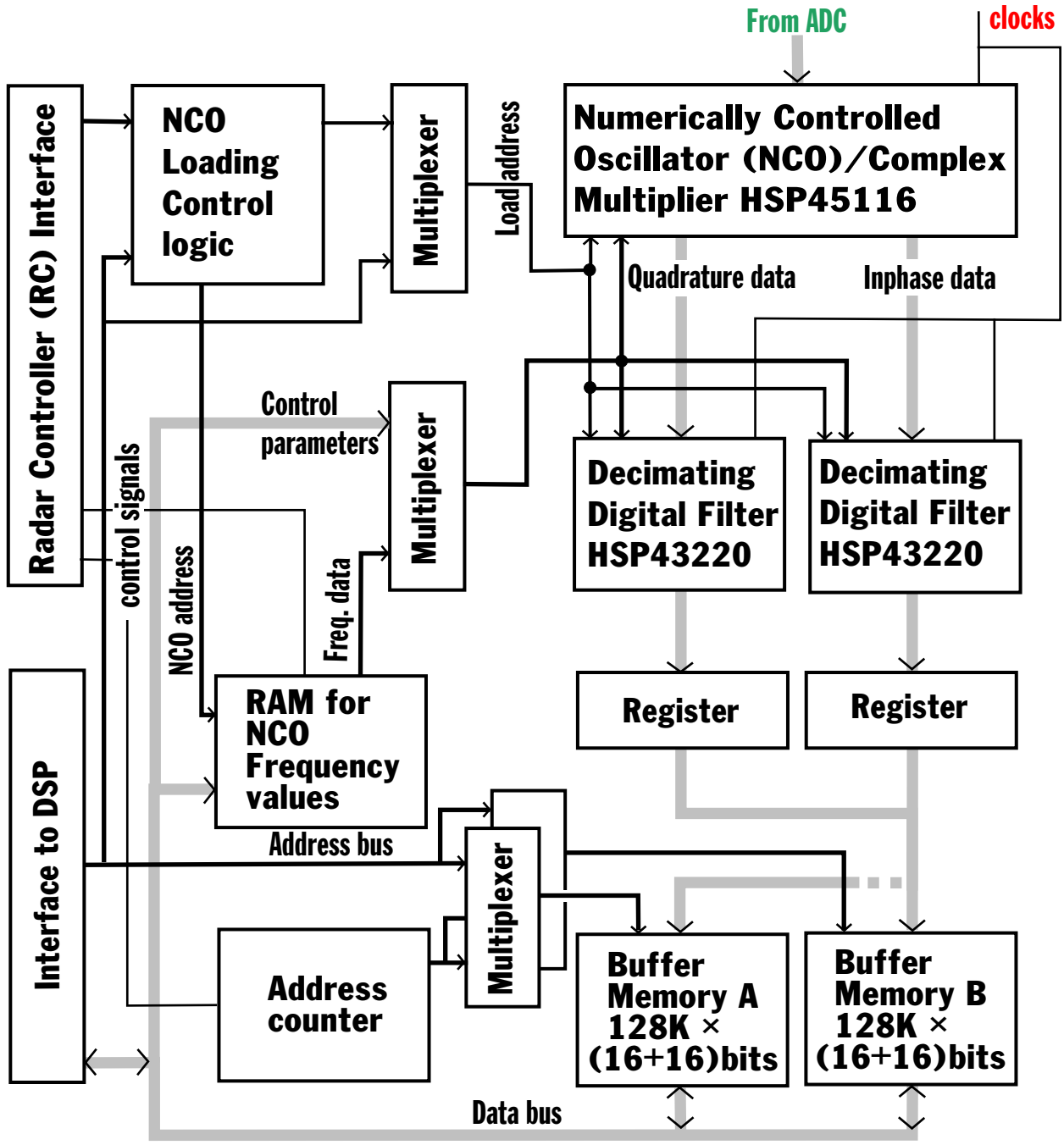
From ADC
clocks
Radar Controller (RC) Interface
NCO Loading Control logic
Multiplexer
Load address
Numerically Controlled Oscillator (NCO)/Complex Multiplier HSP45116
Quadrature data
Inphase data
Control parameters
Multiplexer
control signals
NCO address
Freq. data
Decimating Digital Filter HSP43220
Decimating Digital Filter HSP43220
Register
Register
Interface to DSP
RAM for NCO Frequency values
Address bus
Multiplexer
Address counter
Buffer Memory A 128K × (16+16)bits
Buffer Memory B 128K × (16+16)bits
Data bus